

1

2



1. Status indicator (Power, RUN and ERROR)	8. Expansion port	
2. Model name	9. Expansion unit clip	
3. DIN rail clip	10. DIN rail (35mm)	
4. I/O terminals	11. RS-485 Communication port	
5. I/O point indicator	12. Mounting rail of the Expansion unit	
6. Mounting hole of the Expansion unit	13. DC Power input	
7. Nameplate	14. Expansion port	

2.3 External wiring

DVP04AD-S

Analog Input Module

# Instruction Sheet

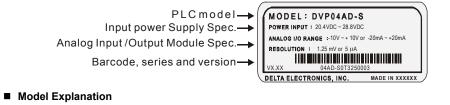
## WARNING

*DVP04AD-S* 

- ${
  m \AA}$  Always read this instruction thoroughly before using the DVP04AD-S.
- $\triangle$  The DC input power must be disconnected before any maintenance.
- ⚠ This is an OPEN-TYPE built-in DVP04AD-S, and the DVP04AD-S is certified to meet the safety requirements of IEC 61131-2 (UL 508) when installed in the enclosure to avoid high temperature, high humidity, exceessive vibration, corrosive gases, liquids, airbome dust or metallic particles. Also, it is equipped with protective methods such as some special tool or key to open the enclosure, so as to avoid the hazard to users and the damage to the DVP04AD-S.
- the DVP04AD-S. Make sure that all the wiring is well conducted prior to power on.
- Do not touch the internal circuit for at least 1 minute after the power supply is disconnected.
- $\triangle$  Make sure that the DVP04AD-S is properly grounded  $(\downarrow)$ , to avoid any electromagnetic noise.

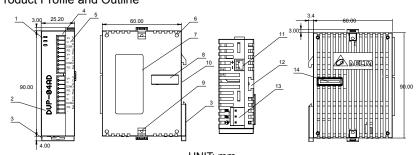
## INTRODUCTION

- 2.1 Model Explanation and Peripherals
- Thank you for choosing DELTA's PLC DVP Series. The analog input module receives external 4-point analog signal input (voltage or current) and transforms it into 14 bits digital signal. The analog input module of DVP04AD-S series can read/write the data of analog input module by using commands FROM / TO via DVP-PLC SS/SA/SX Series MPU program. There are 49 CR(Control Register) in each module and there are 16 bits in each register.
- DVP04AD-S analog input module can update software version by RS-485 communication. Power unit and module are separate. Small volume and easy to install.
- Users can select input from voltage or current via wiring. Voltage input range is ±10V DC (resolution is 1.25 mV). Current input range is  $\pm 20$  mA (resolution is 5  $\mu$ A).
- Nameplate Explanation



#### Serial Number Model 04AD-S 0 T 4 25 0003 Product Series Production series Production week Input + Output points • S: for SS series MPU Production year (2004) for EP series MPU Production place (Taoyuan) H: for EH series MPU - Serial number of version Model type- Production Model XA: Analog input/output mixed module RT: Resistor Thermocouple HC: Input module of high-speed counter PU: single axis positioning unit

AD: Analog input module DA: Analog output module PT: Platinum temperature sensors (PT-100) TC: Thermocouple sensors (Type J/K) 2.2 Product Profile and Outline



~		u wuuug					
					Note 1: Please power v	0	input and othe
	Voltage Input -10V ~ +10V	 	104.7K	CH1	Note 2: If conne short ci termina	rcuit between V	
	= Isolati	on Wire1		4.7K	Note 3: If noise FG to g	is too loud, ple rounding.	ease connect
ŧ	Current Input	Wire at a since the second sec	<sup>1</sup> <sup>1</sup> <sup>1</sup> <sup>1</sup> <sup>1</sup> <sup>1</sup> <sup>1</sup> <sup>1</sup>	04.7K	analog earth po point be machine Note 5: If wave too big please	nodule and input module to pint and make grounding or e cover.	o system system earth connects to al of loaded is feres wiring,
					Warning: DO N termin	OT wire to the nal ●.	No function
2	.4 Termina	al of analog m	odule layout				
	DVP04AD-S	DVP02DA-S	DVP04DA-S	DVP04PT-S	DVP04TC-S	DVP06XA-S	DVP08RT-S
Г							

DVP04AD-S	DVP02DA-S	DVP04DA-S	DVP04PT-S	DVP04TC-S	DVP06XA-S	DVP08RT-S						
0         Vieto           0         0000	n Specificatio		□ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □ □	DUP-84TC 010 ••普古·普古·	DUP-06XA 000 000 000 000 000 000 000 000 000 0	DUP-08RT 000 周 <del>四氣公和</del> 時時國 图 <del>所和的</del> 級公式						
Analog/ Dig	ital (4A/D) module		Voltage input		Current ir	nput						
Power supply volta	age	24 VDC(2	24 VDC(20.4VDC~28.8VDC) ( -15% ~ +20%)									
Analog input chan			/ each module									
Analog input range		±10V			±20 mA							
Digital conversion	range	±8000			1000							
Resolution			<sub>SB</sub> =1.25 mV)		13 bits (1 <sub>LSB</sub> =5 µA)							
Input impedance			200 K Ω 以上 250 Ω									
Overall accuracy			±0.5% of full scale of 25°C (77°F) ±1% of full scale during 0~55°C (32~131°F)									
Response time		3 ms × c	Ų	e (-= ·:·1)								
Isolation Method			It has isolation between digital area and analog area. There is no isolation among channels.									
Absolute input ran	ige	±15 V	±15 V ±32 mA									
Digital data format	t		2's complementary of 16-bit, 13 Significant Bits									
Average function			Yes (CR#2~CR#5 can be set and setting range is K1~K4096)									
Self diagnose fund	ction		Upper and lower bound detection / channels									
		MODBUS	MODBUS ASCII/RTU Mode. Communication baud rate of 4800 / 9600 / 19200 / 38400 /									

Digital data format	2's complementary of 16-bit, 13 Significant Bits
Average function	Yes (CR#2~CR#5 can be set and setting range is K1~K4096)
Self diagnose function	Upper and lower bound detection / channels
Communication mode (RS-485)	MODBUS ASCII/RTU Mode. Communication baud rate of 4800 / 9600 / 19200 / 38400 57600 / 115200. For ASCII mode, date format is 7Bits, even, 1 stop bit (7 E 1). For RTI mode, date format is 8Bits, even, 1 stop bit (8 E 1). The RS-485 is disabled when th DVP04AD-S is connected in series to an MPU.
Connect to DVP-PLC MPU in series	The input point of the first analog expansion unit it connects from the near to the distar is from 0 to 7. The Max, is 8 modules and it won't waste digital I/O point.

## 3.2 Other Specification

4

Power Specification									
Max. Rated Consuming Power 24 VDC(20.4VDC~28.8VDC) ( -15%~+20%), 2W, supply from external power									
Environment Condition									
Environment Condition	Environment Condition It is the same with DVP-PLC MPU.								
Spec. of Prevent Static Electricity All places between terminal and grounding									

## **CR (Control Register)**

		DVP04AD- RS-485	S ai	nalog s	ignal input module				1		Expla	anatio	1	1		1						
	CR No.	Parameter address	La	atched	Register name	b15 b1		-			9 b8	b7	b6	b5	b4	b3	b2		b0			
	#0	H 4000	0	R	Model type		ystem us	sed, c	data le		8bits (b7		DVP(	)4AD-		del co						
	#1	H 4001	0	R/W	Input mode setting		eserved	tina: f	actor	CH4 setting	is H000	CH3			CH2			CH1				
						Mode	e 0: input	t volta	age m	ode (-1	)V~+10\	/).										
							e 1: input e 2: input															
							e 3: input e 4: none			ode(-20	nA~+20	mA)										
	#2	H 4002	0	R/W	CH1 average times	WOUL	- 4. HUHE	: use.														
	#3	H 4003	0	R/W	CH2 average times		e times	settin	ng of o	channel	CH1~C	H4. Se	etting	range	is K1	~K40	96 ar	nd facto	ory			
alog input and other	#4 #5	H 4004 H 4005	$\bigcirc$	R/W R/W	CH3 average times CH4 average times	setting	is K10.															
ent signal, please	#6	H 4006	X	R	average value of																	
een V+ and I+	#7	H 4007	$\sim$	R	CH1 input signal average value of																	
			$\sim$		CH2 input signal	Display	/ average	e valı	ie of (	CH1~CF	14 input	signal										
d, please connect	#8	H 4008	$\times$	R	average value of CH3 input signal	Diopidy	, aronagi	o raio			put	orginai										
· / F · · · · · · · · · · · · · · · · ·	#9	H 4009	$\times$	R	average value of																	
	#10	~ #11	<u> </u>		CH4 input signal	Reserv	/ed															
terminal of	#12	H 400C	$\times$	R	present value of CH1														_			
d 🕀 terminal of	#13	H 400D	X	R	input signal present value of CH2																	
ule to system			()		input signal	Display present value of CH1~CH4 input signal																
ake system earth g or connects to	#14	H 400E	×	R	present value of CH3 input signal																	
	#15	H 400F	$\times$	R	present value of CH4 input signal																	
rminal of loaded is		~ #17	<u>ا</u>	I	•	Reserv	/ed															
interferes wiring,	#18	H 4012	0	R/W	To adjust OFFSET value of CH1														٦			
apacitance with	#19	H 4013	$^{\circ}$	R/W	To adjust OFFSET	Offeet	setting o	f CH1	1~CH	4 Facto	rv settin	g is K0 and unit is LSB.										
	#20	H 4014	$\cap$	R/W	value of CH2 To adjust OFFSET	Voltage	e input: s	etting	g rang	e is K-4	ory setting is K0 and unit is LSB. 4000 ~K4000											
the No function			0		value of CH3	Current	t input: s	etting	g rang	ge is K-4000 ~K4000												
	#21	H 4015	0	R/W	To adjust OFFSET value of CH4																	
	#22			<b>D</b> 44/		Reserv	/ed															
A-S DVP08RT-S	#24	H 4018	0	R/W	To adjust GAIN value of CH1																	
	#25	H 4019	0	R/W	To adjust GAIN value of CH2		setting of							nd un	it is L	SB.						
	#26	H 401A	0	R/W	To adjust GAIN value		e input: s t input: s															
M L <u>3+</u> L <u>3-</u> L4+	#27	H 401B	$\cap$	R/W	of CH3 To adjust GAIN value	ourren	t input. o	oung	y rung		200 10	0400.										
		-			of CH4	_																
	#28 #30	~ #29 H 401E	$\times$	R	Error status	Reserv It is the	ved e data reg	aister	to sa	ve all e	ror statu	is. Ple	ase re	efer to	fault	code (	chart	for det	ail.			
DUP-08R1 000 mm	#31	H 401F	0	R/W	Communication	Setting	RS-485	-														
	#32	H 4020	0	R/W	address setting Communication baud	is K1 It is us	sed to s	set c	ommi	unicatio	baud	rate (	4800	960	). 192	200. 3	38400	. 576	00.			
					rate setting	115200	Obps). Co unication	ommu	unicat	ion form	at: ASC	II mod	le is 7	7Bit, e	ven b	it, 1 s	top bi					
						b0: 4	800 bps	(bit/s	ec).	RIUIII	ł	01:96	00 bp	s (bit/s	sec). (	factor		ing)				
							9200 bp 7600 bp								/sec). it/sec							
						b6-b1	13: reser	vèd.	,	hiah hu							mada)					
							exchang ASCII / F					S che	CK COU	ie (on	IN IOF I	RIUN	noue)					
	#33	H 4021	0	R/W	Reset to factory setting and set		14 b13 eserved	b12	b11	b10 I CH4	b9 b8	b7 CH3	b6	b5	b4 CH2	b3	b2	b1 CH1	b0			
rrent input					characteristics	Factory	y setting					5110		I	5112			2.11	$\neg$			
					adjustable priority		H1 settir hen b0=				FFSET	and (	GAIN	value	of CI	H1 (C	R#18	, CR#	24).			
			1			Wh	hen b0=															
μΑ)						2. b1	R#24). means i		racter	ristic reg	ister is I	atche	d. b1=	0 (fac	tory s	etting	, latch	ned), b	1=1			
er v							ot latched hen b2 is		to 1, a	all settin	gs will re	set to	facto	ry set	ting.							
	#34	H 4022	$\bigcirc$	R	Software version		xadecim									A mea	ans 1	.0A.				
	#35	5~#48 	me	ans lat	System used ched.														_			
e is no isolation among		×	me	ans no	t latched.																	
					read data by using FRC write data by using TO																	
					ignificant Bit): 1. Volta					5mV.	2. Curre	nt inpu	ıt: 1 <sub>LSI</sub>	<sub>B</sub> =20n	nA/40	00=5µ	IA.					
	Evn	lanation:																				
) / 9600 / 19200 / 38400 /	1.			e con	tent of CR#0 is me	odel ty	pe, us	er ca	an re	ead th	e data	fron	n pro	ograr	n to	kno	w if t	here				
		is expa					• •							0								
stop bit (7 E 1). For RTU 485 is disabled when the		CD#1.			used to set 4 inne														_			
185 is disabled when the	2.			ac fai	ur modes to set ar									setti	ng C	H1 t	o mo	ode (	)			
185 is disabled when the om the near to the distant	2.	channe					()1) (] <del> </del>															
the near to the distant /O point.	2.	channe (b2~b0	=0	DO), C	H2 to mode 1(b5-										ode	<u>orvo</u>	ты	he				
185 is disabled when the om the near to the distant	2.	channe (b2~b0 3(b11~l	=0( b9=	00), C =011)	CH2 to mode 1(b5- . It needs to set C	R#1 to									ode	erve	d. T	he				
185 is disabled when the m the near to the distant /O point.	2.	channe (b2~b0 3(b11~l factory	=0 b9= se	00), C =011) tting o	H2 to mode 1(b5-	R#1 to	H068	8 ar	nd th	e upp	er bit (	b12	~b15	5) wil	iode I res							
185 is disabled when the om the near to the distant		channe (b2~b0 3(b11~ factory CR#2 ~ factory	=0 b9= se ~ C se	00), 0 =011) tting o R#5: tting i	CH2 to mode 1(b5- . It needs to set C of CR#1 is H0000. it is used to set av s K10.	R#1 to /erage	H068	8 ar of C	nd th CH1-	e upp -CH4.	er bit ( Settin	b12 <sup>,</sup> g ra	~b15	5) wil is K´	iode I res I~K4	096	and					
485 is disabled when the om the near to the distant /O point.		channe (b2~b0 3(b11~ factory CR#2 ~ factory CR#6 t	=0 b9 se ~ C se	00), C =011) tting c R#5: tting i CR#9	CH2 to mode 1(b5- . It needs to set C of CR#1 is H0000. it is used to set av s K10. are the average v	R#1 to /erage alue th	H068 times hat cale	8 ar of C cula	nd th CH1- tes a	e upp -CH4. accorc	er bit ( Settin ling to	b12 g rai	~b15 nge value	5) wil is K <sup>2</sup> e tha	iode I res I~K4 it is s	096 set ir	and า					
485 is disabled when the om the near to the distant /O point.	3.	channe (b2~b0 3(b11~) factory CR#2 ~ factory CR#6 t CR#2~	=0 b9 se c c se c C R	00), 0 =011) tting o R#5: tting i CR#9 :#5 (a	CH2 to mode 1(b5) . It needs to set C of CR#1 is H0000. it is used to set av s K10. are the average v verage time of Ch	R#1 to verage alue th 11~CH	times times nat calc l4 inpu	8 ar of C cula t sig	nd th CH1- tes a gnal)	e upp -CH4. accord . For (	er bit ( Settin ling to examp	b12 g ra the le, if	~b15 nge value CR#	5) wil is K1 e tha #2 (tl	iode I res I~K4 it is s	096 set ir	and า					
485 is disabled when the om the near to the distant /O point.	3. 4.	channe (b2~b0 3(b11~l factory CR#2 ~ factory CR#6 t CR#2~ of CH1	=0 b9 se c c c c C R ) is	00), C =011) tting c R#5: tting i CR#9 #5 (a 10, i	CH2 to mode 1(b5) . It needs to set C of CR#1 is H0000. it is used to set av s K10. are the average v iverage time of Ch t will calculate the	R#1 to verage alue th 11~CH averag	times times nat calo l4 inpu ge of C	8 ar of C cula t sig CH1	nd th CH1- tes a (nal) inpu	e upp -CH4. accord . For o it sign	er bit ( Settin ing to examp al eve	b12́ g rai the le, if ry 10	value CR#	5) wil is K´ e tha #2 (tl es.	iode I res I~K4 it is s	096 set ir	and า					
185 is disabled when the m the near to the distant /O point.	3.	channe (b2~b0 3(b11~ factory CR#2 ~ factory CR#6 t CR#2~ of CH1 CR#10	=0 b9 se c c c C C C C C C C C C C C C C C C C	00), C =011) tting c R#5: tting i CR#9 2#5 (a 10, i R#11	CH2 to mode 1(b5) . It needs to set C of CR#1 is H0000. it is used to set av s K10. are the average v verage time of Ch	R#1 to verage alue th 11~CH averag CR#22	times times nat calo l4 inpu ge of C 2, CR#	8 ar of C cula t sig CH1 23,	tes a inpu CR#	e upp -CH4. accord . For o it sign #28, C	er bit ( Settin ling to examp al eve R#29	b12́ g rai the le, if ry 10	value CR#	5) wil is K´ e tha #2 (tl es.	iode I res I~K4 it is s	096 set ir	and า					

CR #18~ CR #21: the content is the value of adjusting OFFSET value of CH1~CH4 if analog input voltage or current is 0 after it transfers from analog to digital. Voltage setting range: -5V~+5V(-4000<sub>LSB</sub>~+4000<sub>LSB</sub>). Current setting range: -20mA~+20mA (-4000<sub>LSB</sub>~+4000<sub>LSB</sub>).

8. CR #24~ CR #27: means analog input voltage or current when conversion value from analog signal to digital is 4000. Voltage setting range: -4V~+20V(-3200<sub>LSB</sub>~+16000<sub>LSB</sub>). Current setting range: -16mA~+52mA(-3200LSB ~+10400LSB). But it needs to notice that GAIN VALUE -OFFSET VALUE =  $+800_{LSB} + 12000_{LSB}$  (voltage) or  $+800_{LSB} + 6400_{LSB}$  (current). When this value under this range, the resolution of the input signal will be thin and the variation of value will be larger. When this value exceeds this range, the resolution of input signal will be thick and the variation of value will be smaller

### CR#30 is fault code. Please refer to the follo

CR#30 IS Iault COUE. F				<u> </u>						
Fault description	Content	b15~b8	b7	b6	b5	b4	b3	b2	b1	b0
Power source abnormal	K1(H1)		0	0	0	0	0	0	0	1
Analog input value error	K2(H2)		0	0	0	0	0	0	1	0
Setting mode error	K4(H4)		0	0	0	0	0	1	0	0
Offset/Gain error	K8(H8)	Reserved	0	0	0	0	1	0	0	0
Hardware malfunction	K16(H10)	Reserved	0	0	0	1	0	0	0	0
Digital range error	K32(H20)		0	0	1	0	0	0	0	0
Average times setting error	K64(H40)		0	1	0	0	0	0	0	0
Command error	K128(H80)		1	0	0	0	0	0	0	0
Note: Each fault code will have corresponding bit (b0~b7). Two or more faults may happen at the same time. 0 means normal and 1 means having fault.										

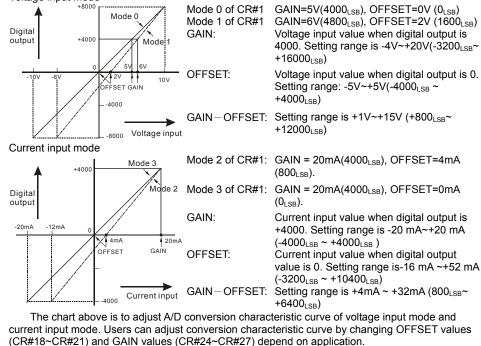
- 10. CR#31: it is used to set RS-485 communication address. Setting range is 01~255 and factory setting is K1.
- 11. CR#32 is used to set RS-485 communication baud rate: 4800, 9600, 19200, 38400, 57600, 115200 bps. b0: 4800bps. b1: 9600bps. (factory setting) b2: 19200bps. b3: 38400 bps. b4: 57600 bps. b5: 115200 bps. b6-b13: reserved. b14: exchange low and high byte of CRC check code. (only for RTU mode) b15=0: ASCII mode. b15=1: RTU mode.
- 12. CR#33 is used to set the inner function priority. For example: characteristic register. Output latched function will save output setting in the inner memory before loss power.
- CR#34: software version.
- 14. CR#35~ CR#48: system used.
- The corresponding parameters address H4000~H4030 of CR#0~CR#48 can provide user to 15 read/write data by RS-485.
  - Communication baud rate: 4800, 9600, 19200, 38400, 57600, 115200 bps.
  - Communication format: ASCII mode is 7Bit, even bit, 1 stop bit (7 E 1). Communication B. format of RTU mode is 8Bit, even bit, 1 stop bit (8 E 1).
  - С Function code: 03H—read data from register. 06H—write a WORD into register. 10H—write many WORDs into register

#### Adjust A/D Conversion Characteristic Curve

#### 5.1 Adjust A/D Conversion Characteristic Curve

#### Voltage input mode

5



LSB(Least Significant Bit): 1. voltage input: 1<sub>LSB</sub>=10V/8000=1.25mV. 2. current input: 1<sub>LSB</sub>=20mA/4000=5µA.

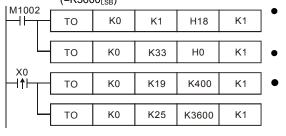
5.2. Program Example for Adjusting A/D Conversion Characteristics Curve

Example 1: setting OFFSET value of CH1 to 0V(=K0LSB) and GAIN value of CH1 to 2.5V(=K2000LSB).

M1002	-[	то	K0	K1	H0	K1	•	Writing H0 to CR#1 of analog input module no. 0 and set CH1 to mode 0 (voltage input -10V~+10V)
	Г						1	(voltage liput - 10v~+ 10v)
		TO	K0	K33	H0	K1	•	Writing H1 to CR#33 and allow to
X0	_						-	adjust characters of CH1.
⊢ĨŤ⊢⊤	-	то	K0	K18	K0	K1	•	When X0 switches from OFF to ON,
							_	K0 <sub>LSB</sub> of OFFSET value will be wrote in
	-	то	K0	K24	K2000	K1		CR#18 and K2000 <sub>LSB</sub> of GAIN value will be wrote in CR#24.

- odule no. 0 and set CH1 to mode 0 oltage input -10V~+10V) iting H1 to CR#33 and allow to just characters of CH1.
- nen X0 switches from OFF to ON, LSB of OFFSET value will be wrote in

Example 2: setting OFFSET value of CH2 to 2mA(=K400 LSB) and GAIN value of CH2 to 18 mA (=K3600<sub>LSB</sub>)



#### no. 0 and set CH2 to mode 3 (current input: -20 mA ~ +20mA) • Writing H0 to CR#33 and allow to adjust

characteristics of CH4 When X0 switches from OFF to ON,

Writing H18 to CR#1 of analog input mode

K400<sub>LSB</sub> of OFFSET value will be wrote in CR#19 and K3600<sub>LSB</sub> of GAIN value will be wrote in CR#25

### **Initial PLC Start-up**

#### Lamp display:

- When power is on, POWER LED will be lit and ERROR LED will be lit for 0.5 second. When it is normal that POWER LED should be lit and ERROR LED should turn off. When 2. power supply is lower than 19.5V, ERROR LED will blink continuously till the power supply
- is higher than 19.5V. When it connects to PLC MPU in series, RUN LED on MPU will be lit and A/D LED or D/A LED should blink.
- After receiving the first RS-485 command during controlling by RS-485, A/D LED or D/A 4. LED should blink.
- 5 After converting, ERROR LED should blink if input or output exceeds upper bound or lower than lower bound

Example: v1000					
	FROM	K0	K0	D0	K1
M1002					
	то	K0	K1	H3030	K1
	то	K0	K2	K32	K2
- = H88 D0	FROM	K0	K6	D20	K4
	END				

#### Explanation:

Program

Example

- Reading the data of model type from expansion module K0 and distinguish if the data is H88 (DVP04AD-S model type).
- If the model type is DVP04AD-S, M11 is on and the setting input mode is (CH1, CH3)= mode 0, (CH2 CH4) = mode 3
- Setting the average times of CH1 and CH2 are K32.
- Reading the input signal average value of CH1~CH4 (4 data) saving in D20~D23.

#### **Related Instructions Explanation**

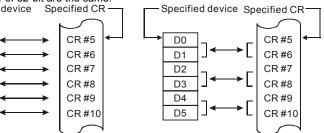
AP					~			_		_				Sp	eci	cial module CR Adaptive model				
78		D	F	ĸ	0	VI	Р	(m1)	) ( <u>m</u> 2	) ( <u>s</u>	) (	n	•			read out ES EP EH				
		_					_							aa						
$\setminus$	Bit device Word device													16-bit command (9 STEPS)						
	Х	Υ	Μ	S	Κ	Н	KnX	KnY	KnM	KnS	Т	С	D	Е	F	Continuous				
$m_1$					*	*										FROM execution				
$m_2$					*	*														
D								*	*	*	*	*	*	*	*					
n					*	*										DFROM Continuous DFROMP Pulse				
	<ul> <li>Note: The usage range of operand m₁ is 0~7.</li> </ul>										execution execution									
The usage range of operand m <sub>2</sub> : ES/EP:										• Flag: When M1083=On, it allows to										
	0-48, EH: 0-254.																			
	The usage range of operand n: ES/EP: n= FROM/TO.																			
	1~(49-m2), EH: 1~(255-m2). Refer to following for detail.																			
	ES series model doesn't support pulse												reler to following for detail.							
	execution command (FROMP, DFROMP).																			

- $(m_1)$ : the number for special module.  $(m_2)$ : the number of CR (Control Register) of Command special module that will be read. D: the location to save reading data. C: the Explanation data number of reading one time.
  - DVP-series PLC uses this command to read CR data of special module.
  - D: When assigning bit operand, K1~K4 can be used for 16-bit and K5~K8 can be ٠
  - used for 32-bit. ٠
  - Please refer the following footnote for calculating of special module number.
  - ◆ To read the content of CR#24 of special module#0 to D0 of PLC and to read the content of CR#25 of special module#0 to D1 of PLC. It can read 2 data in one time (n=2).

			Н×	0	-	FRO	M	K	D	K	24		D0	Τ	K2						
	API 79	0	то		P	m	) (	<u>m</u> 2)	(S		Ð		ecia a wi		nodule e in	CR	Ada ES	aptive m	odel EH		
					_				_								$\checkmark$	~	~		
		Bit de	vice M S	К	н	KnX		KnM			С	DE	: T F	16	6-bit com	mano	1 (9 ST	EPS)			
	m <sub>1</sub> m <sub>2</sub> S			* *	* *	*	*	*	*	*	*	* *		т	O Con	tinuou cution	IS TO	Pul	se cution		
	n			*	*	Ť	Ť	Ť	T	Ť	Ŧ	<u>т</u> 1	` ^	32	2-bit com	mano	I (17 S	TEPS)			
	•	Note				le range of operand m₁ is 0~7. ge range of operand m₂: ES/EP: 0-48,									DTO Continuous DTOP Pulse execution						
			EH	: 0-2	254.	0		•		-			-	L				=On, it a			
														rupt duri	ng						
For ES series, it doesn't support pulse Refer to following for de												detail.									
execution command (TOP, DTOP)																					
(	Comm Explan		•	o d	of sp lata	ecial num	moo ber t	dule t o wri	hat v te in	vill t one	e w	rote e.	in. ⊂	S	: the dat	a to w	rite in (	ntrol Reg CR. ท	): the		
			•	_	_												•	al module			
			•			Whe sed fo			ng b	it op	erar	nd, K	1~K4	l ca	an be us	ed for	16-bit	and K5~	K8 can		
	Progi Exam		•															CR#3 and ).	d CR#2		
Ň	<ul> <li>example</li> <li>of special module#0. It only writes a group of data in one time (n=1).</li> <li>The command will be executed when X0=ON and it won't be executed when X0=OFF. The data that wrote in previous won't have any change.</li> </ul>																				
				() 	[	DT	0	K	0	ĸ	(2	Τ	D0		K1	]					
				_	. L							_				1					
ſ	Foot	note	) •	TI				nmar	•					ا ب ام	la Tha m				ماريام		
(			)	•														ecial mo odule fro			
												from	0 to	7. '	The max	imum	is 8 sp	ecial mo	odules		
				٠				ccup				า 16-	bit of	49	groups	memo	ory of s	pecial m	odule		
					(#0	~#48	3). Al								er of CR alues of s			al digital le has			
				٠		ludec sina		M/T	О со	mma	and.	the	unit a	of re	ead/write	of Cl	R is one	e numbe	r for		
					one	e time	e. If u	using	DF									e of CR			
					nur	nber: J	s in a Jppe	one t er 16	-bit	Lo	wer	16-k	oit								
						Г		R #1			CR		_∢		Specif	ied C	R num	her			
				• •	The	num							 s n. T					5-bit com	nmand		
1			_			d n=1 ified		32-bit	are Spe				_		Snecifie	d davi		cified Cl	D		
				0	pee	meu	uevi		ope.			ì			opeome		ce ope				
J			L	→[	DO	, , ,	<	;		CR	#5	┥	L		D0	-		CR #5	┥		
				Ľ	D1		<	;	•	CR					D1		→L	CR #6			
				╞	D2	_	<b>←</b>	;		CR					D2	]≁_	- <b>→</b> Г	CR #7			
				┝	D3 D4					CR CR					D3 D4	_ _	<b>L</b>	CR #8 CR #9			
				F	D5	_	<	;			#10				D5	]←	<b>→</b> [	CR #10			
				-		-			l	٢	$\frown$	1					l	$\bigcirc$	1		
			•	In I				nand				3 ie 1	not n	rov				nen n=3 O comr	nand ie		
			•															routines			

◆ The command will be executed when X0=ON. The command won't be executed when X0=OFF and the content of previous reading data won't change.

M K0 K24	D0	K2
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executed, all interrupts (including external or internal interrupt subroutines) will be disabled. All interrupts will be executed after FROM/TO command is completed. Besides, FROM/TO command also can be executed in the interrupt subroutine.

◆ The function of the flag M1083 (FROM/TO mode exchange) provided in EP/EH series models

1. When M1083=Off, FROM/TO command is executed, all interrupts (including external or internal interrupt subroutines) will be disabled. All interrupts will be executed after FROM/TO command is completed. Besides, FROM/TO command also can be executed in the interrupt subroutine.

2. When M1083=On, if an interrupt occurs while FROM/TO command has been programmed, FROM/TO command will be interrupted to execute the interrupt. However, FROM/TO command cannot be executed in the interrupt subroutine.